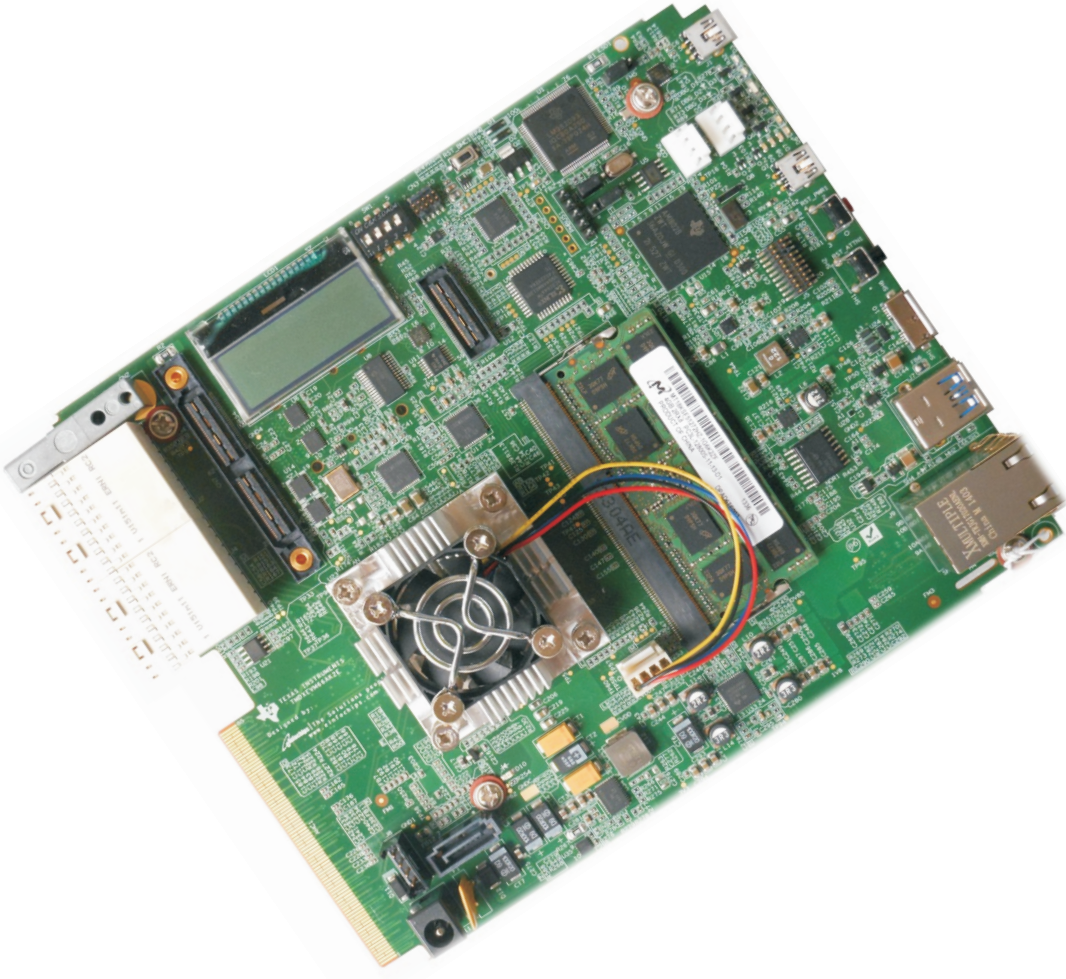

EVMK2E Evaluation Module Quick Start Guide

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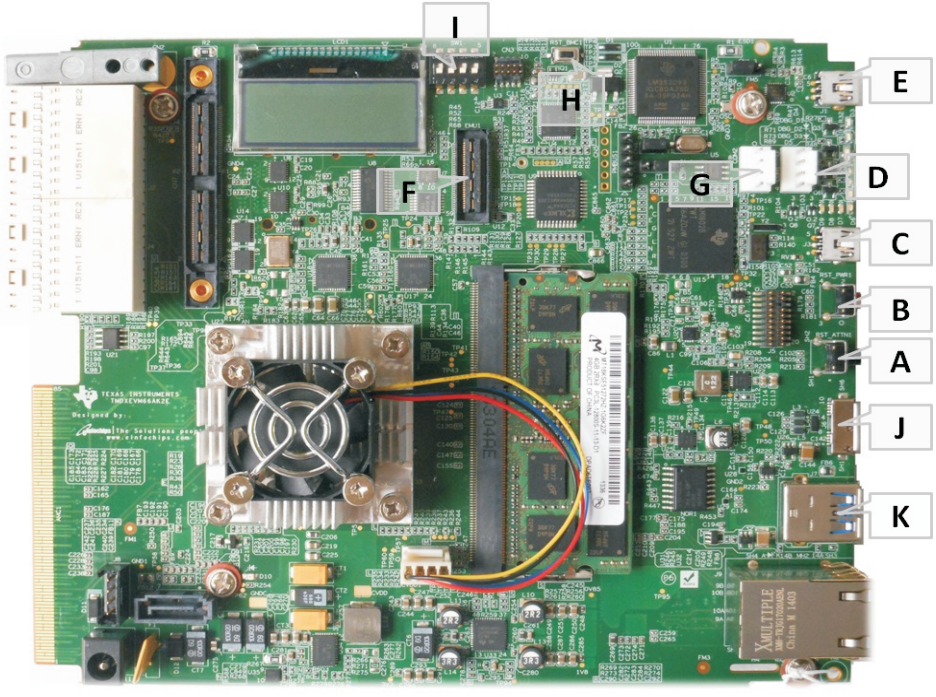
EVMK2E Evaluation Module



A	No Functionality	F	JTAG(MIPI-60) connector for SoC
B	1 press: safe shutdown of SOC; 2 presses within 0.5sec: warm reset; 3 presses within 0.5sec: full reset; 4 presses within 0.5sec: cancel reset	G	COM2 : BMC UART Console
C	On board XDS200 USB	H	BMC Reset : Resets the microcontroller and will reset the entire board
D	COM1 : SoC UART Console	I	Dip switch for boot configuration : 1111 : No Boot mode/JTAG DSP Little Endian 0010 : NOR SPI ARM Master Boot
E	USB control of both SOC COM1 & BMC COM2	J	USB3 PORT 1(Device and HOST Modes)
		K	USB3 PORT 0(HOST only)

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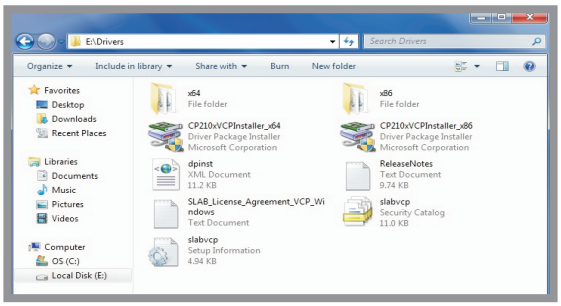
Two boot procedures are described below, one for Uboot and one for CCS.

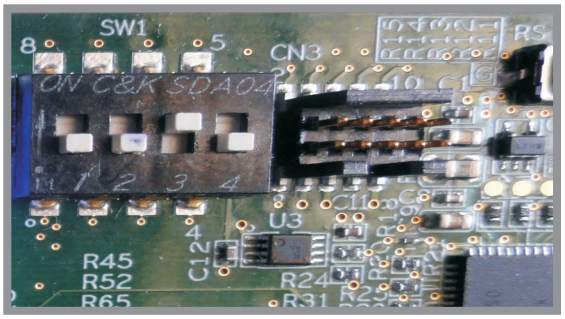
1) Uboot Steps:



1

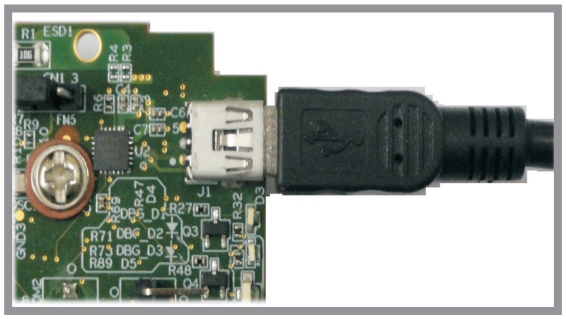
For Windows User:
Insert pendrive and load USB COM drive by executing the program\drivers\ CP210xVCPInstaller_x86
For 64-bit systems execute the program\drivers\

For Linux User:
Please download driver from any of below link as per Linux kernel version on PC. Compile the driver and install it.
http://www.silabs.com/Support%20Documents/Software/Linux_3.x.x_VCP_Driver_Source.zip
http://www.silabs.com/Support%20Documents/Software/Linux_2.6.x_VCP_Driver_Source.zip



2

Ensure EVM configuration switches are set as shown for NOR SPI Boot.



3

Connect the USB mini-B cable to J1

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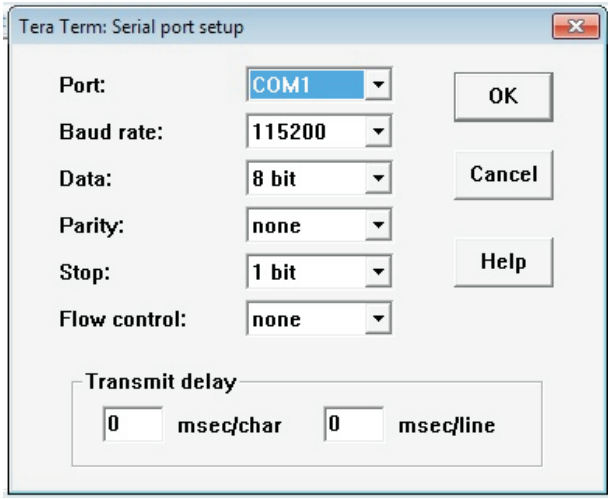


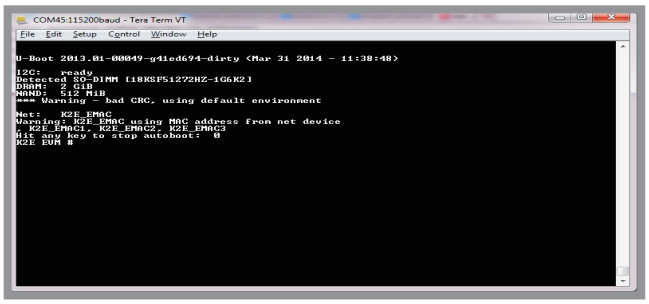
4

Open Tera Term,
select COM port, configure
settings, as shown in image.

This serial port settings of
Tera Term is shown as an
example. It is not mandatory
to use only Tera Term as
a serial terminal

5

Insert DC-in cable to apply
12V power.



6

On SoC serial port console
a display will be seen,
as shown in image.
Hit any key to stop
auto-boot when prompted.
EVM is now ready for
Uboot applications.

MCSDK Downlink Page:
http://software-dl.ti.com/sdoemb/sdoemb_public_sw/mcsdk/latest/index_FDS.html

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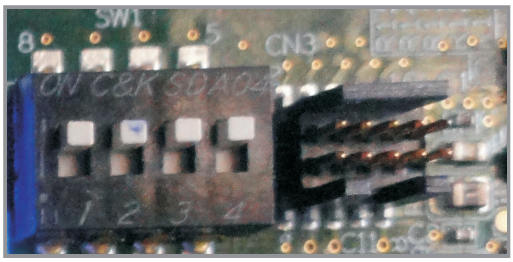
2) CCS Steps:

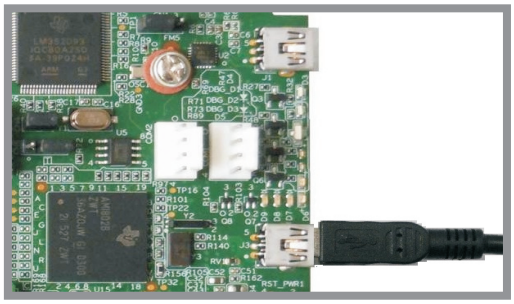
1

Update or install CCS (V5.5.0.00077 or later) from MCSDK download page.



2

Ensure EVM configuration switch are set as shown for No Boot/JTAG DSP Little Endian Boot mode.



3

Connect the USB mini-B cable to XDS200.

4

Insert DC-in cable to apply 12V power.

5

Launch CCS. After CCS is opened, EVM is now ready for use.

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Getting Started:
Please see the 'Getting Started' chapter in the MCSDK User Guide for hardware and software setup:
http://processors.wiki.ti.com/index.php/MCSDK_User_Guide_for_KeyStone_II.

Important Notes when handling the EVM:

Plug in all the cables before powering ON the EVM to ensure proper grounding.
Do not unnecessarily flex the boards, while inserting cables. Excessive flexing can break the PCB traces.
Use proper ESD procedures when handling the EVM.
Warning: Some components on the EVM may be too hot to touch with your bare hand when power is on.

Online Documentation

TI device product page
http://www.ti.com/product/66ak2e05

Multicore SDK
http://www.ti.com/tool/bioslinuxmcsdk
http://processors.wiki.ti.com/index.php/MCSDK_User_Guide_for_KeyStone_II

CCS v5
http://processors.wiki.ti.com/index.php/Category:Code_Composer_Studio_v5

C663X EVM Information:
https://www.einfochips.com/tl-66ak2e-evm

This link contains:

Hardware & Software details
User Guide
Technical Reference Manual

EVMK2E EVM Support:
Please write to tievmsupport@einfochips.com for technical queries related to this EVM.

eInfochips Address:
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Note: The European Free Call (Toll Free) number is not active in all countries. If you have technical difficulty calling the free call number, please use the international number above.
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Hong Kong	800-96-5941	Philippines	1-800-765-7404
India	1-800-425-7888	Singapore	800-886-1028
Indonesia	001-803-8861-1006	Taiwan	0800-006800
Korea	080-551-2804	Thailand	001-800-886-0010

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